

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.:

Patent No.:

Filed:

Issue Date:

Title:

Commissioner for Patents  
Washington, D.C. 20231

**POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST  
(REVOCATION OF PRIOR POWERS)**

As assignee of record of each of the patents listed in the table of attachment A,

**REVOCATION OF PRIOR POWERS OF ATTORNEY**

all powers of attorney previously given in each of the listed patents are hereby revoked, and

**NEW POWER OF ATTORNEY**

the following attorneys/agents are hereby appointed to prosecute this patent/application and to transact all business in the Patent and Trademark Office connected therewith: I hereby appoint all attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Customer Number shown below as the attorneys to prosecute this patent/application and to transact all business in the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number:

**15650**

Patent Trademark Office

Please direct all future correspondence and telephone calls to:

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**ASSIGNEE OF ENTIRE INTEREST**

**BROADCOM CORPORATION**

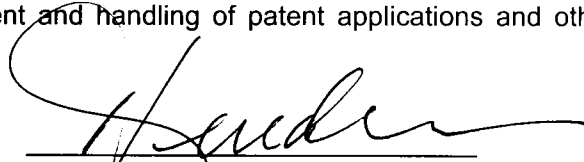
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Irvine, California 92617-3038

**ASSIGNEE CERTIFICATION**

The certification under 37 C.F.R. §3.73(b) establishing the right of assignee to take action is attached hereto along with documentation evidencing same. Further, in my official position with Broadcom Corporation, I am authorized to sign documents and otherwise act on its behalf in connection with the management and handling of patent applications and other intellectual property matters.

Date: \_\_\_\_\_

9/12/11

  
\_\_\_\_\_  
**Dee Henderson**  
Director - Intellectual Property Administration  
Broadcom Corporation

### Attachment A

| No. | Patent No | BD No.     | Patent Title   | Issue Date | BD Assign't<br>(Reel/Frame) |
|-----|-----------|------------|--|------------|-----------------------------|
| 1   | 7,443,812 | BU1340C1   | VOICE AND DATA EXCHANGE OVER A PACKET BASED NETWORK WITH AGC                                   | 10-28-2008 | 023060/0615                 |
| 2   | 7,924,752 | BU1340C2   | VOICE AND DATA EXCHANGE OVER A PACKET BASED NETWORK WITH AGC                                   | 04-12-2011 | 023060/0615                 |
| 3   | 6,850,577 | BU1268I1C1 | VOICE AND DATA EXCHANGE OVER A PACKET BASED NETWORK WITH TIMING RECOVERY                       | 02-01-2005 | 010818/0778                 |
| 4   | 7,529,325 | BU1268I1C2 | VOICE AND DATA EXCHANGE OVER A PACKET BASED NETWORK WITH TIMING RECOVERY                       | 05-05-2009 | 010818/0778                 |
| 5   | 6,661,427 | BU1263     | GRAPHICS DISPLAY SYSTEM WITH VIDEO SCALER  | 12-09-2003 | <u>010681/0057</u>          |
| 6   | 6,700,588 | BU1264     | APPARATUS AND METHOD FOR BLENDING GRAPHICS AND VIDEO SURFACES                                  | 03-02-2004 | <u>010701/0460</u>          |
| 7   | 7,057,627 | BU1480     | VIDEO AND GRAPHICS SYSTEM WITH SQUARE GRAPHICS PIXELS  | 06-06-2006 | <u>011892/0230</u>          |
| 8   | 7,336,287 | BU1480C1   | VIDEO AND GRAPHICS SYSTEM WITH SQUARE GRAPHICS PIXELS  | 02-26-2008 | 011892/0230                 |
| 9   | 7,719,547 | BU1480C2   | VIDEO AND GRAPHICS SYSTEM WITH SQUARE GRAPHICS PIXELS  | 05-18-2010 | 011892/0230                 |
| 10  | 6,570,579 | BU1202     | GRAPHICS DISPLAY SYSTEM  | 05-27-2003 | <u>010643/0928</u>          |
| 11  | 6,975,324 | BU1233.5   | VIDEO AND GRAPHICS SYSTEM WITH A VIDEO TRANSPORT PROCESSOR                                     | 12-13-2005 | <u>011671/0033</u>          |
| 12  | 7,538,783 | BU1263C1   | GRAPHICS DISPLAY SYSTEM WITH VIDEO SCALER  | 05-26-2009 | 010681/0057                 |
| 13  | 7,920,151 | BU1263C2   | GRAPHICS DISPLAY SYSTEM WITH VIDEO SCALER  | 04-05-2011 | 010681/0057                 |
| 14  | 7,002,602 | BU1264C1   | APPARATUS AND METHOD FOR BLENDING GRAPHICS AND VIDEO SURFACES                                  | 02-21-2006 | 010701/0460                 |
| 15  | 7,489,729 | BU1856C1   | METHOD AND APPARATUS FOR IMPLEMENTING REDUCED MEMORY MODE FOR HIGH-DEFINITION TELEVISION       | 02-10-2009 | <u>021093/0200</u>          |
| 16  | 7,444,455 | BU1900D1   | INTEGRATED GIGABIT ETHERNET PCI-X CONTROLLER   | 10-28-2008 | 015735/0148                 |
| 17  | 6,741,243 | BU1428     | METHOD AND SYSTEM FOR REDUCING OVERFLOWS IN A COMPUTER GRAPHICS SYSTEM                         | 05-25-2004 | <u>011572/0759</u>          |
| 18  | 7,548,622 | BU1441     | SYSTEM AND METHOD FOR THE CONCEALMENT OF DEVICE INPUT PARAMETERS                               | 06-16-2009 | <u>011971/0515</u>          |
| 19  | 7,546,099 | BU1540     | BLUETOOTH BASEBAND SOLUTION WITH REDUCED PROCESSOR REQUIREMENTS AND INTEGRATED HOST CONTROLLER | 06-09-2009 | <u>017831/0590</u>          |
| 20  | 7,149,474 | BU1606     | WIRELESS CABLE REPLACEMENT SYSTEM  | 12-12-2006 | <u>017831/0593</u>          |
| 21  | 7,187,916 | BU5349     | METHOD AND SYSTEM FOR MEASURING RECEIVER MIXER IQ MISMATCH                                     | 03-06-2007 | <u>018075/0976</u>          |

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|-----|-----------|----------|--|------------|-----------------------------|
| 22  | 7,596,363 | BU5349C1 | METHOD AND SYSTEM FOR MEASURING RECEIVER MIXER IQ MISMATCH   | 09-29-2009 | 0140808/0052                |
| 23  | 6,987,821 | BU1281   | VOICE AND DATA EXCHANGE OVER A PACKET BASED NETWORK WITH SCALING ERROR COMPENSATION                                      | 01-17-2006 | <u>011344/0805</u>          |
| 24  | 6,967,946 | BU1332   | VOICE AND DATA EXCHANGE OVER A PACKET BASED NETWORK WITH PRECISE TONE PLAN   | 11-22-2005 | <u>012113/0953</u>          |
| 25  | 6,486,887 | BU1432   | METHOD AND SYSTEM FOR IMPROVING COLOR QUALITY OF THREE-DIMENSIONAL RENDERED IMAGES                                       | 11-26-2002 | <u>010872/0970</u>          |
| 26  | 6,396,360 | BU1657   | METHOD AND CIRCUITRY FOR IMPLEMENTING AN INDUCTOR-CAPACITOR PHASE INTERPOLATED VOLTAGE-CONTROLLED OSCILLATOR             | 05-28-2002 | <u>012572/0599</u>          |
| 27  | 6,888,578 | BU2109   | METHOD AND SYSTEM FOR CROSS-LUMINANCE REDUCTION IN MULTI-STANDARD TELEVISION DECODERS                                    | 05-03-2005 | <u>012912/0771</u>          |
| 28  | 7,362,251 | BU5375   | METHOD AND SYSTEM FOR DIGITAL TO ANALOG CONVERSION FOR POWER AMPLIFIER DRIVER AMPLITUDE MODULATION                       | 04-22-2008 | <u>017955/0449</u>          |
| 29  | 7,548,180 | BU5375C1 | METHOD AND SYSTEM FOR DIGITAL TO ANALOG CONVERSION FOR POWER AMPLIFIER DRIVER AMPLITUDE MODULATION                       | 06-16-2009 | 017955/0449                 |
| 30  | 7,865,030 | BU5383   | METHOD AND SYSTEM FOR MOTION COMPENSATED TEMPORAL FILTERING USING BOTH FIR AND IIR FILTERING                             | 01-04-2011 | <u>018949/0350</u>          |
| 31  | 6,980,528 | BU1357.1 | VOICE AND DATA EXCHANGE OVER A PACKET BASED NETWORK WITH COMFORT NOISE GENERATION  | 12-27-2005 | <u>011608/0996</u>          |
| 32  | 6,731,296 | BU1416   | METHOD AND SYSTEM FOR PROVIDING PROGRAMMABLE TEXTURE PROCESSING  | 05-04-2004 | <u>010903/0946</u>          |
| 33  | 6,559,685 | BU1659   | REGENERATIVE SIGNAL LEVEL CONVERTER  | 05-06-2003 | <u>012024/0668</u>          |
| 34  | 7,787,491 | BU5423   | METHOD AND SYSTEM FOR SYNCHRONIZABLE E-VSB ENHANCED DATA INTERLEAVING AND DATA EXPANSION                                 | 08-31-2010 | <u>018309/0401</u>          |
| 35  | 6,762,756 | BU1409C2 | GRAPHICS PROCESSOR, SYSTEM AND METHOD FOR GENERATING SCREEN PIXELS IN RASTER ORDER UTILIZING A SINGLE INTERPOLATOR       | 07-13-2004 | 010892/0812                 |
| 36  | 5,926,181 | BU1410   | METHOD AND APPARATUS FOR IDENTIFYING AND ELIMINATING THREE-DIMENSIONAL OBJECTS VISUALLY OBSTRUCTED FROM A PLANAR SURFACE | 07-20-1999 | <u>010901/0110</u>          |
| 37  | 6,624,819 | BU1430   | METHOD AND SYSTEM FOR PROVIDING A FLEXIBLE AND EFFICIENT PROCESSOR FOR USE IN A GRAPHICS PROCESSING SYSTEM               | 09-23-2003 | <u>010873/0793</u>          |
| 38  | 6,937,251 | BU1432C1 | METHOD AND SYSTEM FOR IMPROVING COLOR QUALITY OF THREE-DIMENSIONAL RENDERED IMAGES                                       | 08-30-2005 | 010872/0970                 |
| 39  | 7,099,278 | BU1851   | LINE LOOP BACK FOR VERY HIGH SPEED APPLICATION   | 08-29-2006 | <u>012085/0192</u>          |

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|-----|-----------|------------|--|------------|-----------------------------|
| 40  | 7,463,864 | BU5352     | MODIFIED DUAL BAND DIRECT CONVERSION ARCHITECTURE THAT ALLOWS EXTENSIVE DIGITAL CALIBRATION                        | 12-09-2008 | <u>018075/0976</u>          |
| 41  | 7,818,028 | BU5352C1   | MODIFIED DUAL BAND DIRECT CONVERSION ARCHITECTURE THAT ALLOWS EXTENSIVE DIGITAL CALIBRATION                        | 10-19-2010 | 018075/0976                 |
| 42  | 6,185,263 | BU1223     | ADAPTIVELY CONFIGURABLE CLASS-A/CLASS-B TRANSMIT DAC FOR TRANSCEIVER EMISSION AND POWER CONSUMPTION CONTROL        | 02-06-2001 | <u>010260/0513</u>          |
| 43  | 5,963,210 | BU1409     | GRAPHICS PROCESSOR, SYSTEM AND METHOD FOR GENERATING SCREEN PIXELS IN RASTER ORDER UTILIZING A SINGLE INTERPOLATOR | 10-05-1999 | <u>010892/0812</u>          |
| 44  | 6,456,284 | BU1409C1   | GRAPHICS PROCESSOR, SYSTEM AND METHOD FOR GENERATING SCREEN PIXELS IN RASTER ORDER UTILIZING A SINGLE INTERPOLATOR | 09-24-2002 | 010892/0812                 |
| 45  | 6,285,348 | BU1414     | METHOD AND SYSTEM FOR PROVIDING IMPLICIT EDGE ANTIALIASING   | 09-04-2001 | <u>010897/0886</u>          |
| 46  | 6,775,667 | BU1429     | METHOD AND SYSTEM FOR PROVIDING A HARDWARE SORT FOR A LARGE NUMBER OF ITEMS  | 08-10-2004 | <u>010873/0751</u>          |
| 47  | 7,440,504 | BU2062     | METHOD AND APPARATUS FOR PERFORMING DEBLOCKING FILTERING WITH INTERLACE CAPABILITY                                 | 10-21-2008 | <u>013424/0646</u>          |
| 48  | 7,057,622 | BU1205C1   | GRAPHICS DISPLAY SYSTEM WITH LINE BUFFER CONTROL SCHEME  | 06-06-2006 | 010608/0307                 |
| 49  | 6,768,774 | BU1233.3   | VIDEO AND GRAPHICS SYSTEM WITH VIDEO SCALING   | 07-27-2004 | <u>011657/0447</u>          |
| 50  | 7,991,049 | BU1233.3C  | VIDEO AND GRAPHICS SYSTEM WITH VIDEO SCALING   | 08-02-2011 | 011657/0447                 |
| 51  | 6,573,905 | BU1233.6   | VIDEO AND GRAPHICS SYSTEM WITH PARALLEL PROCESSING OF GRAPHICS WINDOWS   | 06-03-2003 | <u>013948/0254</u>          |
| 52  | 6,608,630 | BU1205     | GRAPHICS DISPLAY SYSTEM WITH LINE BUFFER CONTROL SCHEME  | 08-19-2003 | <u>010608/0307</u>          |
| 53  | 7,227,582 | BU1209C    | GRAPHICS DISPLAY SYSTEM WITH VIDEO SYNCHRONIZATION FEATURE   | 06-05-2007 | 011185/0189                 |
| 54  | 7,545,438 | BU1209C1   | GRAPHICS DISPLAY SYSTEM WITH VIDEO SYNCHRONIZATION FEATURE   | 06-09-2009 | 011185/0189                 |
| 55  | 6,870,538 | BU1233.6C1 | GRAPHICS DISPLAY SYSTEM WITH VIDEO SYNCHRONIZATION FEATURE   | 06-09-2009 | 019848/0254                 |
| 56  | 7,659,900 | BU1233.6C3 | VIDEO AND GRAPHICS SYSTEM WITH PARALLEL PROCESSING OF GRAPHICS WINDOWS   | 02-09-2010 | 010643/0928                 |
| 57  | 7,365,752 | BU1233.7C1 | VIDEO AND GRAPHICS SYSTEM WITH A SINGLE-PORT RAM   | 04-29-2008 | 011607/0583                 |